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مدار منطقی و معماری کامپیوتر:

۱- کتاب مدرسان شریف

۲- اصول طراحی دیجیتال تألیف واکرلی

۳- طراحی سخت‌افزار مهندسی کامپیوتر تألیف موریس مانو

۴- کتاب طراحی سیستم‌های کامپیوتری تألیف مانو

۵- کتاب معماری کامپیوتر تألیف پترسون

مجموعه دروس در سطح کارشناسی ارشد:

معماری کامپیوتر پیشرفته:

۱- کتاب مدرسان شریف

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## VLSI پیشرفته:

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- 5- S. Yalamachalli, "Introductory VHDL from simulation to Synthesis".
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- 17- S. Goel, S. Gollamudi, A. Kumar, M. Bayoumi, "Energy Efficient 1-Bit Full-Adder Cells for Low Voltage", The Center for Advanced Computer Studies, University of Louisiana at Lafayette.
- 18- N. Khandel, K. Navi, "The Design of a High-Performance Full-Adder Cell by Combining Common Digital Gates and Majority Function", Department of Electrical and Computer Engineering and Microelectronics Research Center, Shahid Beheshti University.
- 19- J. Alfredsson, S. Aunet, "Trade-offs for High Yield in 90 nm Sub-threshold Floating-gate Circuits by Monte Carlo Simulations", Department of Information Technology and Media, Mid Sweden University.
- 20- S. R. Chowdhury, A. Banerjee, A. Roy, H. Saha, "A high Speed 8 Transistor Full-Adder Design using Novel 3-Transistor XOR Gates", International Journal of Electronics, Circuits and Systems.
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